

FIG. 1

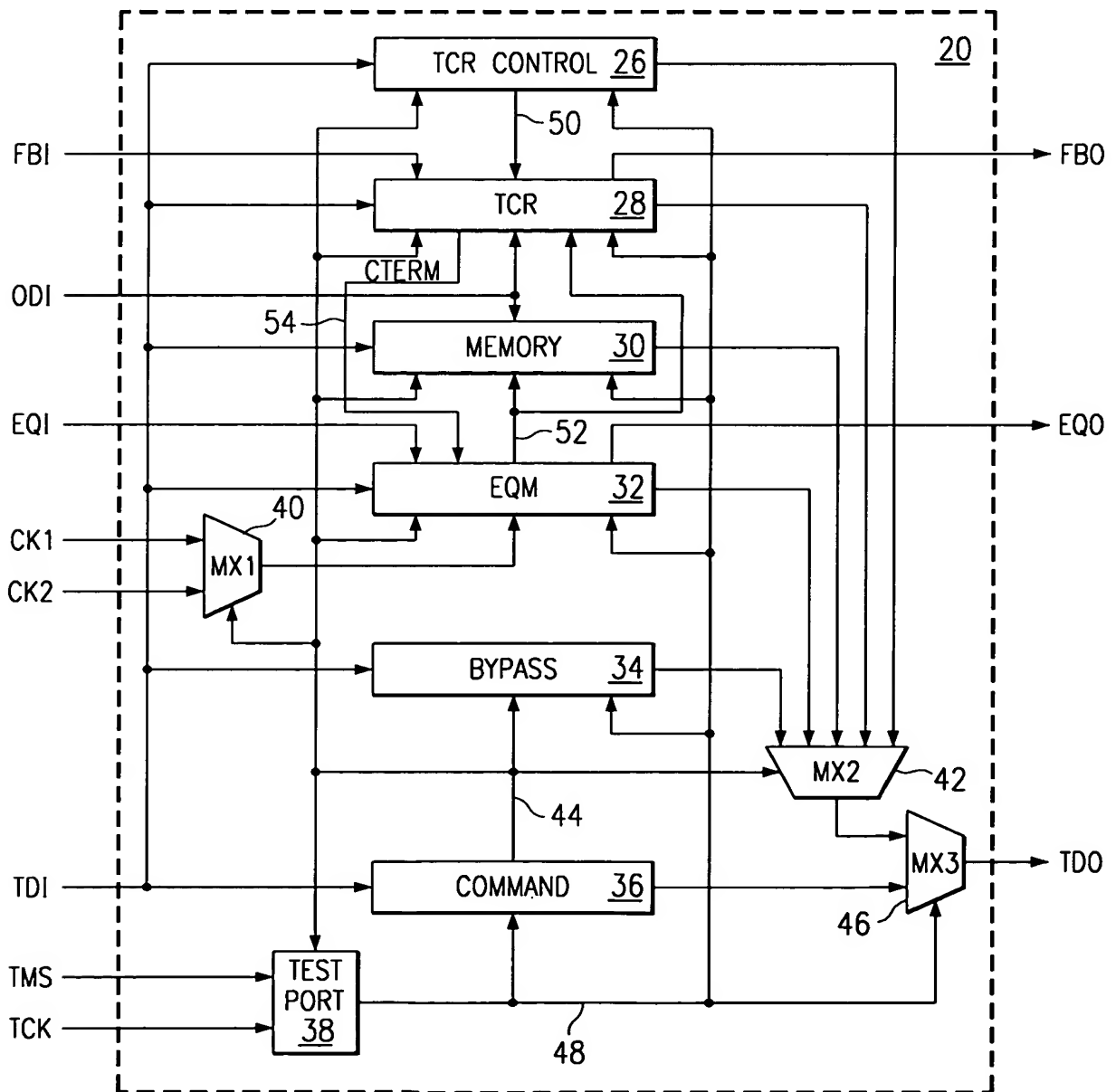


FIG. 2

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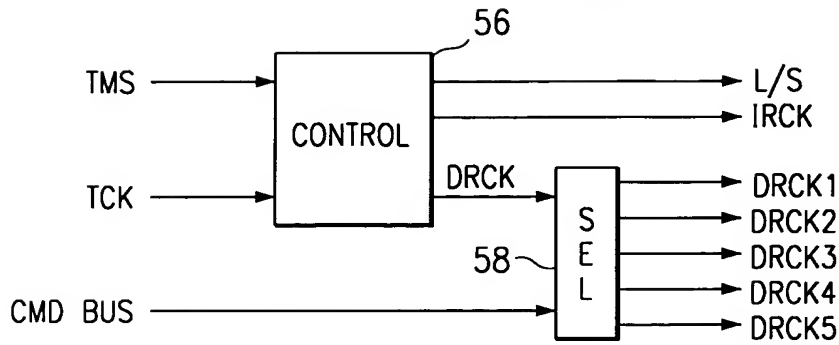


FIG. 3

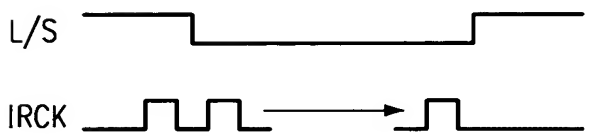


FIG. 3a

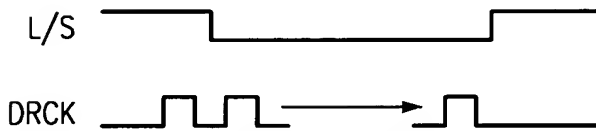


FIG. 3b

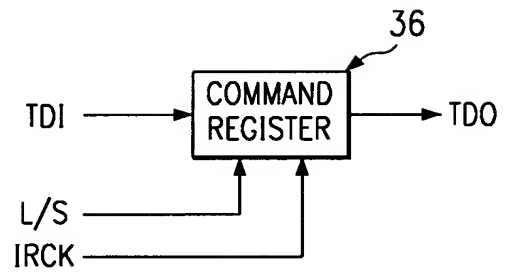


FIG. 4

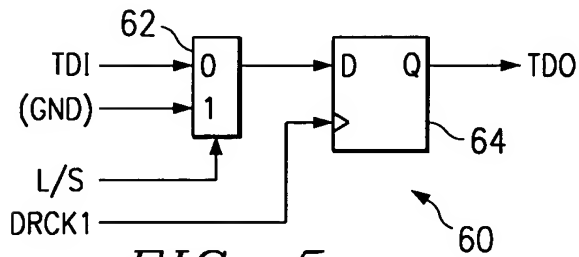


FIG. 5

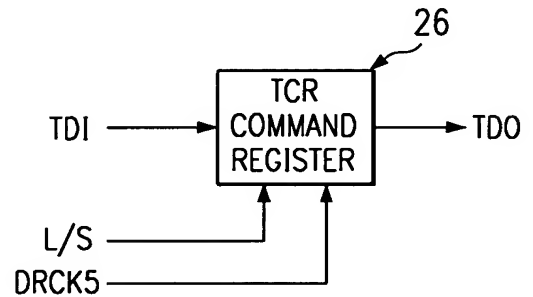


FIG. 6

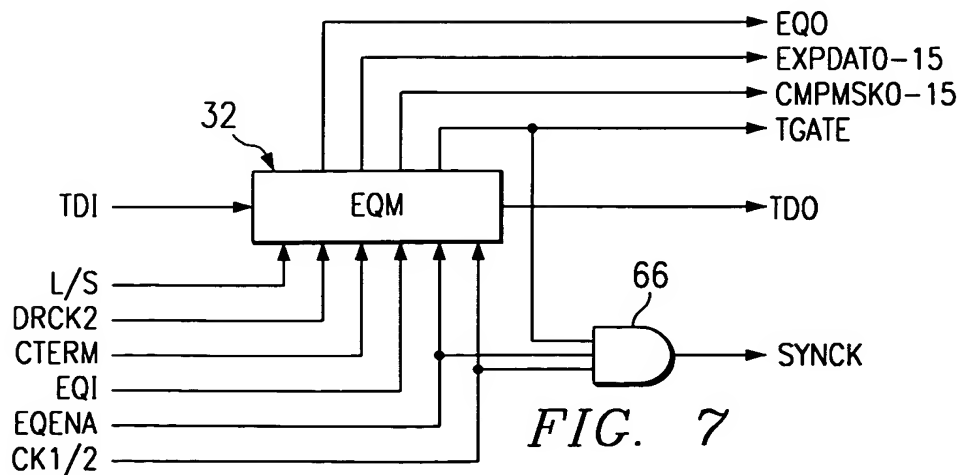


FIG. 7

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FIG. 7a

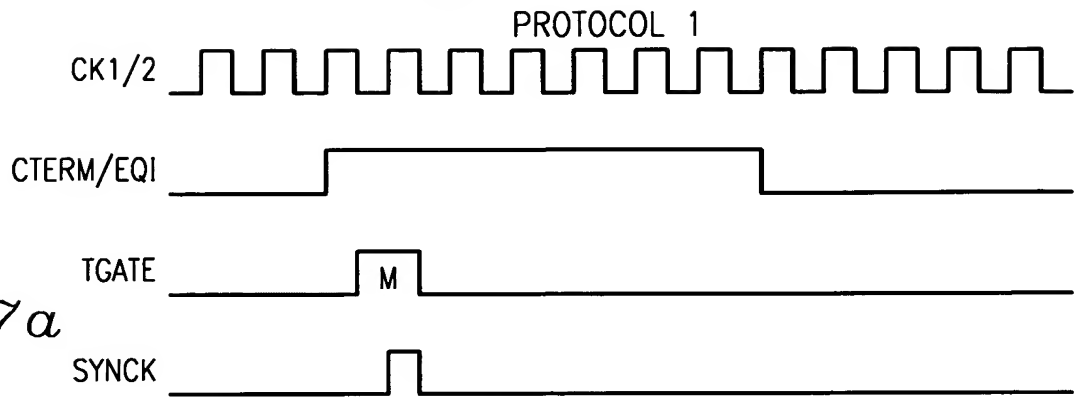


FIG. 7b

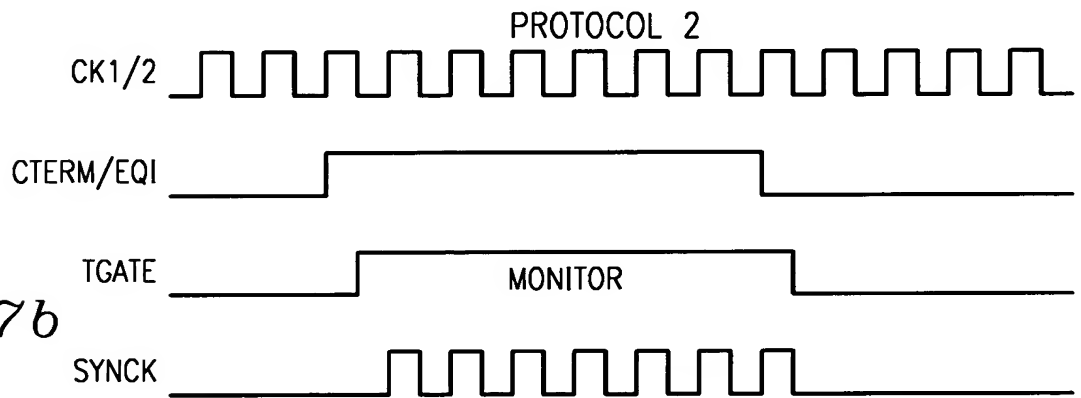


FIG. 7c

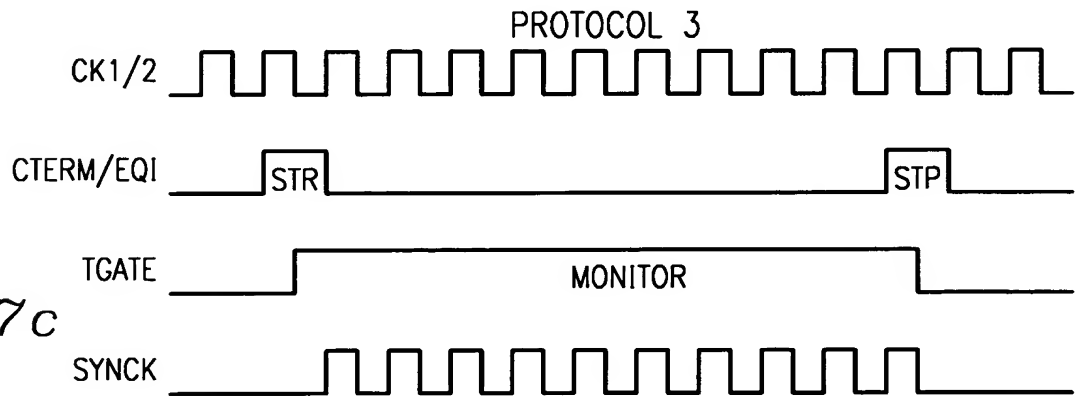
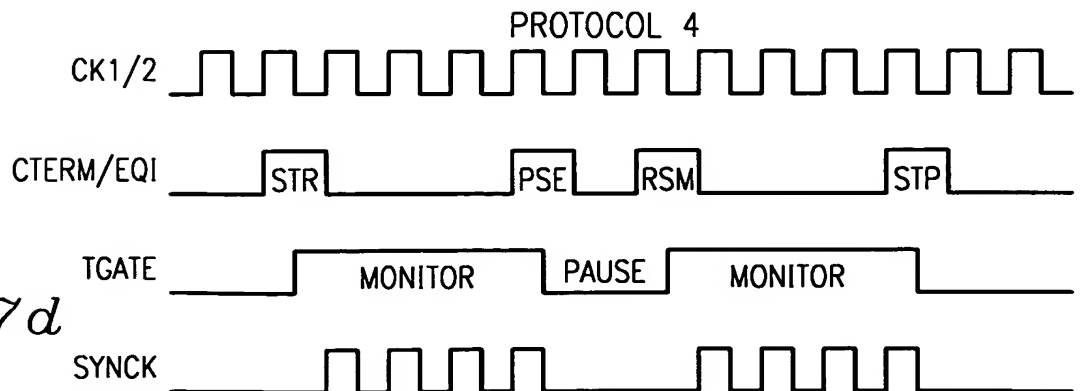
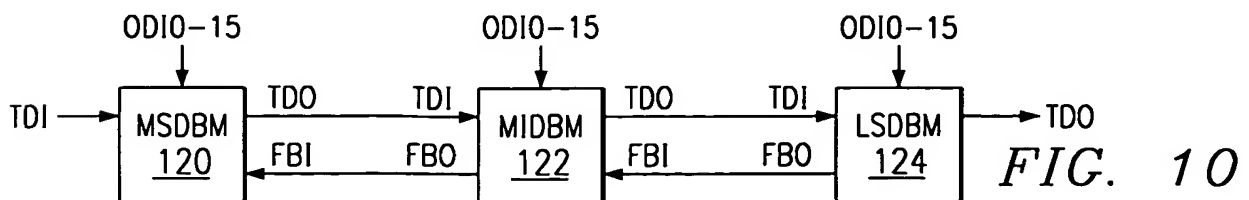
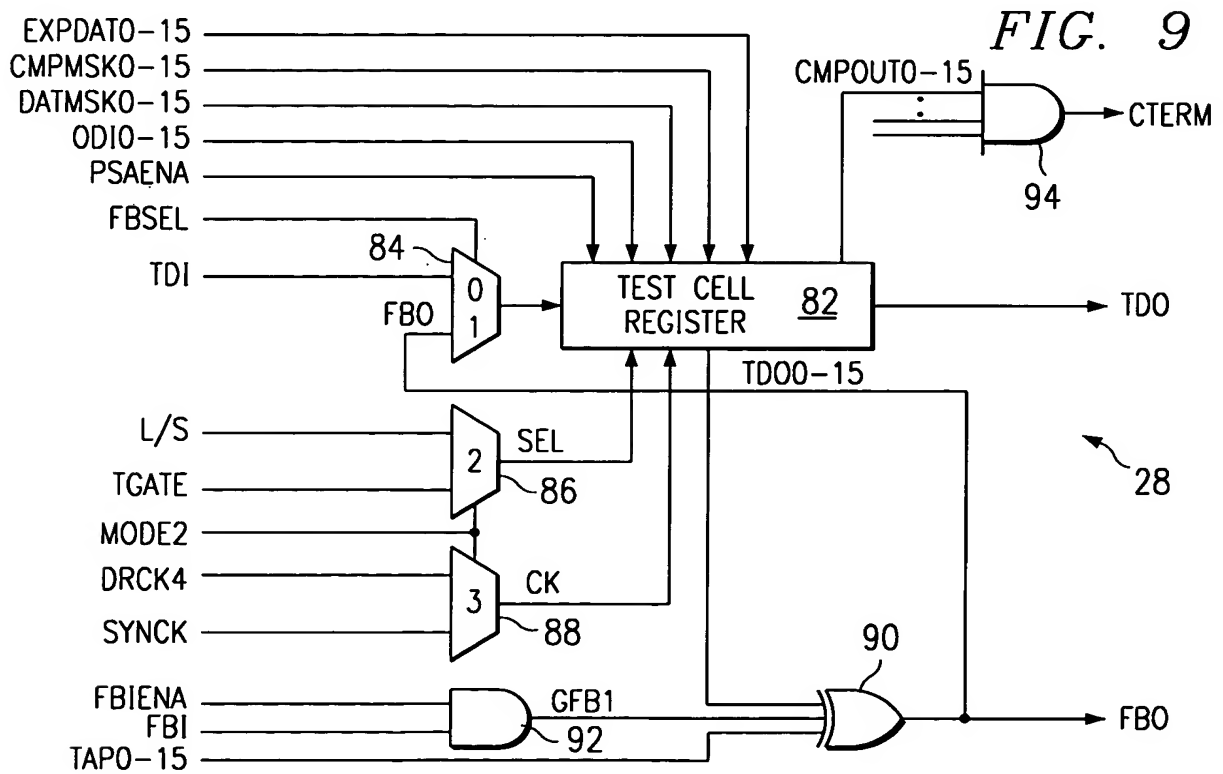
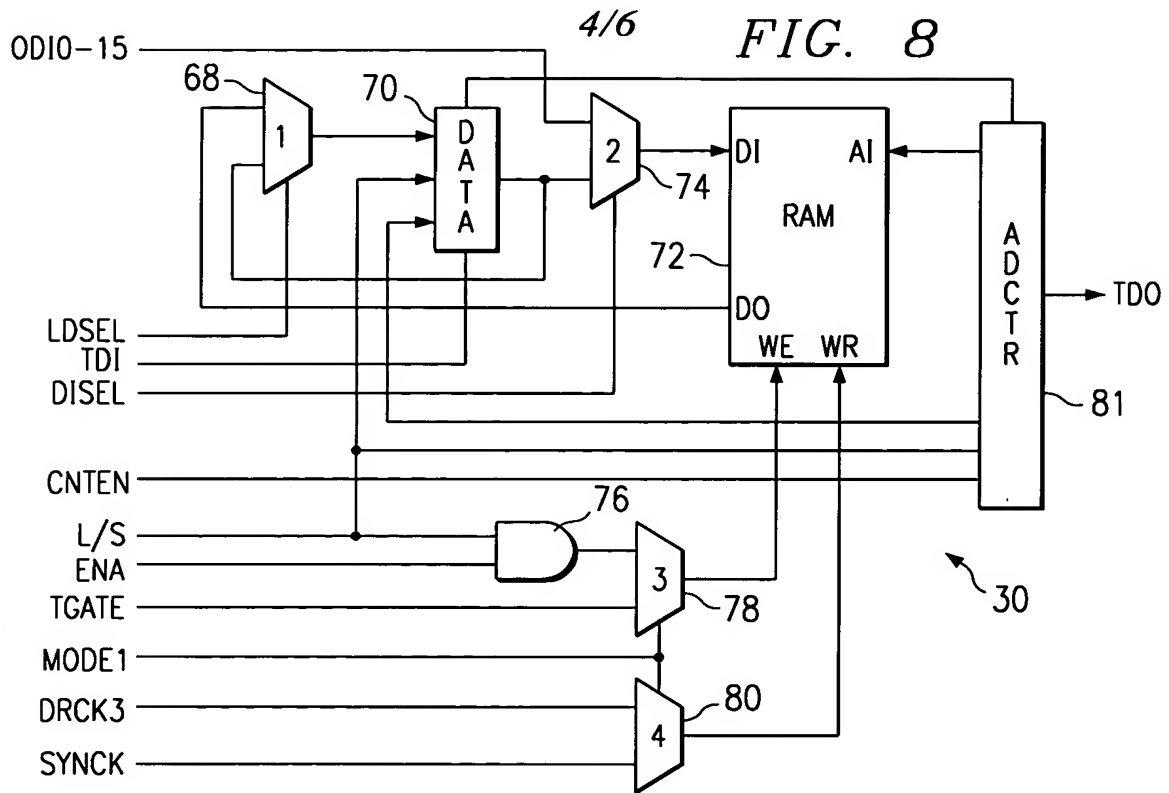


FIG. 7d





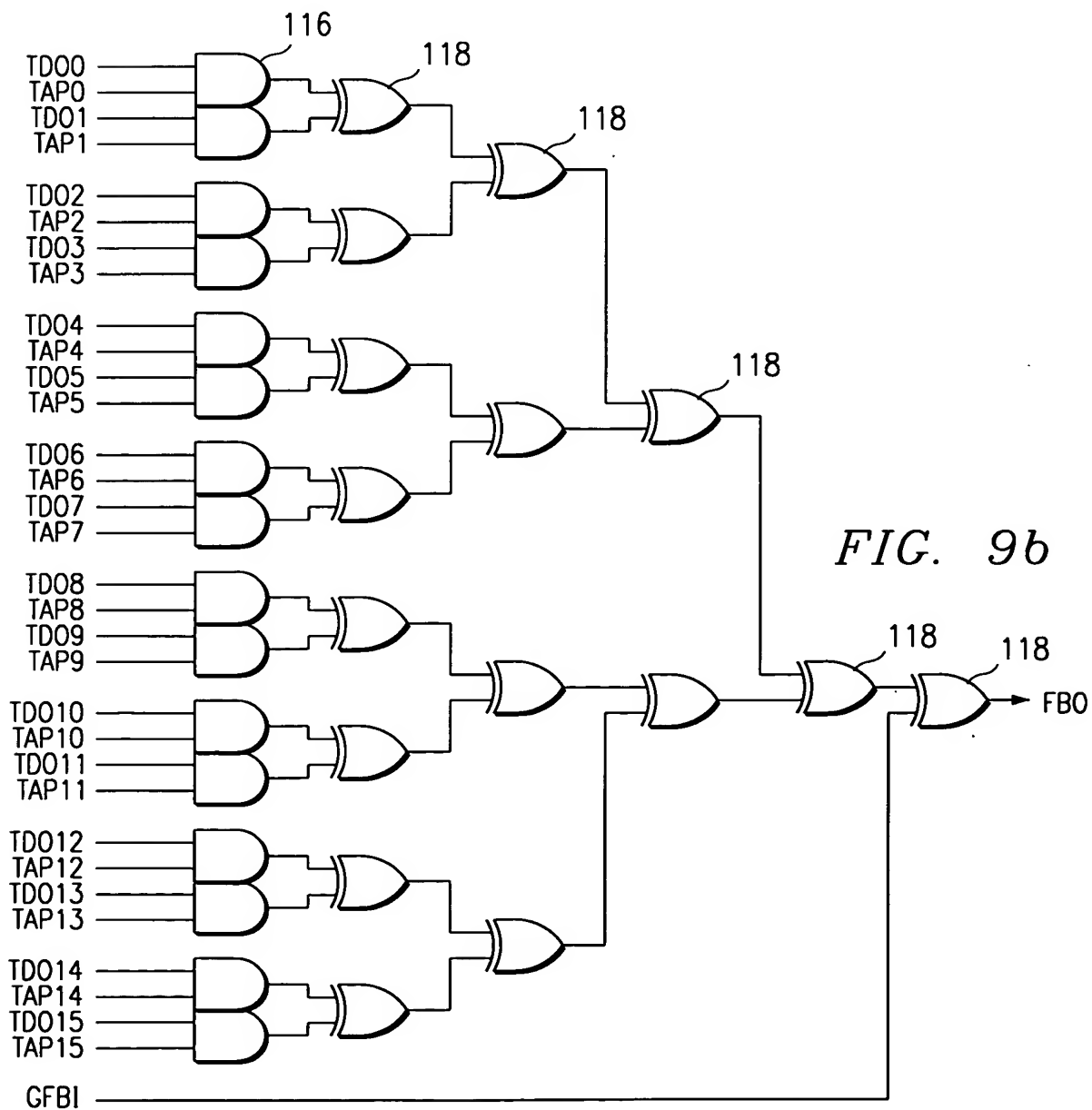
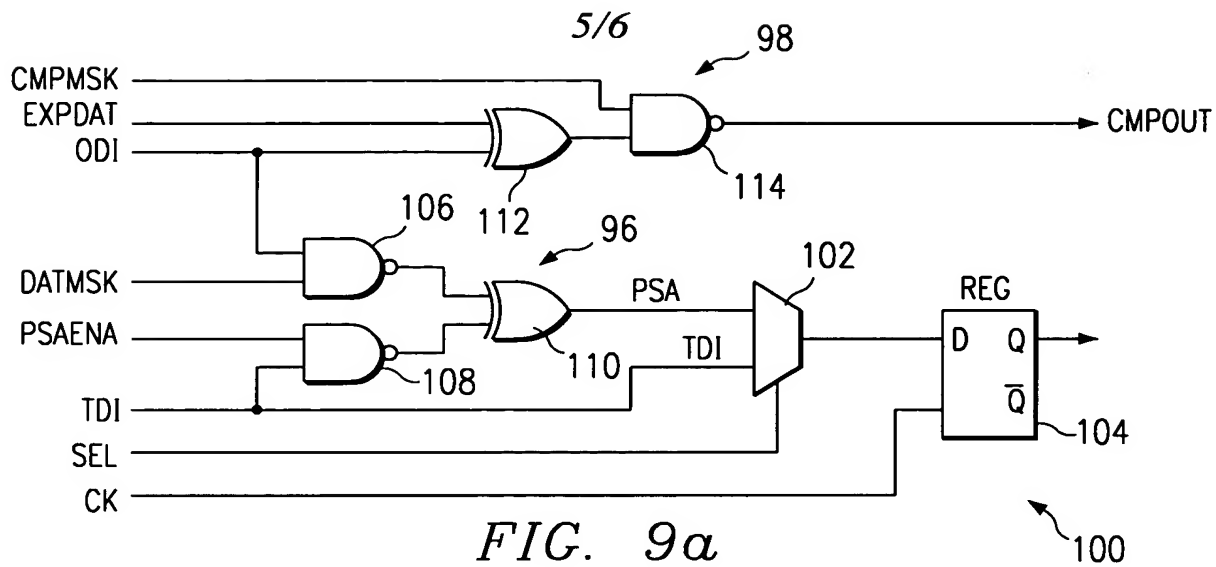
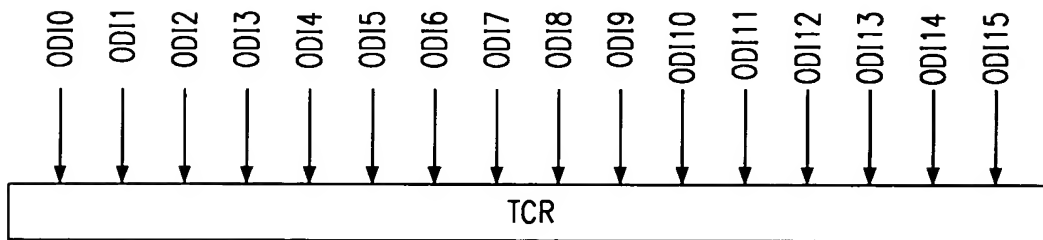


FIG. 9c

PERFORMING PARALLEL SIGNATURE ANALYSIS
ON ALL ODI INPUTS

*FIG. 9d*

PERFORMING SINGLE SIGNATURE ANALYSIS
ON ONE ODI INPUT

